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	Application Number
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Attorney Docket No: 303.860US1	
Sheet 1 of 1	

US PATENT DOCUMENTS				
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
MC	US-6,687,146	02/03/2004	Kurjanowicz, Wlodek, et al.	01/31/2002

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Examiner Initials *	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T ²

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
m		DEFFREE, SUSAN, "IBM proves vertical transistor DRAM at 70nm", <u>Electronic News</u> , (June 10, 2003), 2 pgs	
		HERGENROTHER, J M., et al., "The vertical replacement-gate (VRG) MOSFET: a 50-nm vertical MOSFET with lithography-independent gate length", <u>Bell Laboratories, Lucent Technologies, Murray Hill</u> , 4pgs	
		ISA, "IBM claims speediest silicon transistor", http://www.isa.org/Template.cfm?Section=Communities&template=/TaggedPage/DetailDisplay.cfm&ContentID=20445 , ISA - The Instrumentation, Systems and Automation Society, (November 4, 2002), 2 pgs	
		LUCENT TECHNOLOGIES, "Revolutionary transistor design turns the silicon world on end", http://www.bell-labs.com/news/1999/november/15/1.html , Lucent Technologies, Bell Labs Innovations, (1999), 3 pgs	
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m		NING, T H., "Why BICMOS and SOI BiCMoS?", <u>IBM</u> , 0018-8646, (11.29.01), 7 pgs	

EXAMINER

Matthew C. Fisk

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